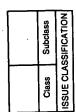
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PA	TENT	NUMBER

## U.S. UTILITY PATENT APPLICATION

PATENT DATE

					ART UNIT EXAN	AINER 1/0/35.1
- 1	APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	TOCCEN
	09/970701	F	705	26	2162	

Shigenobu Maeda

Method for collecting semiconductor devices and method for selling and using semiconductor devices

## PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION										
ORIGII	ORIGINAL				CROSS REFERENCE(S)					
CLASS SUBCLASS		CLASS		SUBCLASS (ONE SUBCLASS PER BLOCK)						
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INTERNATIONAL C	CLASSIFICATION		=							
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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
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a) The term of this patent subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)			
b) The term of this patent shall						
not extend beyond the expiration date of U.S Patent. No				ISSUE FEE		
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c) The terminalmonths of this patent have been disclaimed.		nents Examiner)	(Date)			
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